

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated May 14, 2007, has been received and its contents carefully reviewed.

Claims 1-15 are rejected to by the Examiner. Claims 1, 4, 10 and 12 have been amended. No new matter has been added. Claims 1-15 remain pending in this application.

In the Office Action, claims 1-15 are rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 6,037,920 to Mizutome et al. (hereinafter "Mizutome") in view of U.S. Patent No. 6,727,875 to Mikami et al. (hereinafter "Mikami") and further in view of U.S. Patent No. 4,789,899 to Takahashi (hereinafter "Takahashi").

The rejection of claims 1-15 is respectfully traversed and reconsideration is requested. Claims 1-9 are allowable over the cited references in that each of these claims recites a combination of elements including, for example, "a gate driving circuit for applying substantially identical scan pulses at least twice to each one of the plurality of gate lines during one frame period of the LCD panel; and a data driving circuit for applying data voltages having the same gray scale value at least twice to the data lines of the LCD panel in synchrony with the scan pulses." Claims 10-15 are allowable over the cited references in that each of these claims recited a combination of elements including, for example, "applying substantially identical scan pulses at least twice to each of the plurality of gate lines during one frame period of the LCD panel; and applying data voltages having the same gray scale value at least twice to the plurality of data lines in synchrony with the scan pulses." None of the cited references including Mizutome, Mikami and Takahashi, singly or in combination, teaches or suggests at least these features of the claimed invention.

In the Office Action, the Examiner states Takahashi teaches "the liquid crystal display comprising a gate driving circuit for applying substantially identical scan pulses twice to each one of the plurality of gate lines during one frame period of the LCD panel." However, Takahashi only discloses, in col. 2 lines 51 - 61 and col. 2, line 67 – col.3, line 11, applying image signals in pixel elements in the first half of each field scanning period and a direct current potential V_H in the pixel elements in the second half of each field scanning period. From the disclosure, it is apparent that the image signals which are supplied through a shift register 1 and a sample and hold circuit 2 are different from the direct current potential V_H which is supplied through switching elements S_{21} , S_{22} Consequently, Takahashi does not disclose "a gate

driving circuit for applying substantially identical scan pulses at least twice to each one of the plurality of gate lines during one frame period of the LCD panel; and a data driving circuit for applying data voltages having the same gray scale value at least twice to the data lines of the LCD panel in synchrony with the scan pulses.”

Accordingly, Applicants respectfully submit that claims 1 and 10 are allowable over Mizutome, Mikami and Takahashi.

Claims 2-9 and 11-15 each depend respectively from one of claims 1 and 10, and each includes by reference all of the limitations from the respective base claims. Accordingly, Applicants respectfully submit that claims 2-9 and 11-15 are each allowable over Mizutome, Mikami and Takahashi, by way of their respective dependencies, and for the reasons given above for their respective base claims 1 and 10.


Applicants believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. *A duplicate copy of this sheet is enclosed.*

Respectfully submitted,

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